ST 2SC3195

NPN Silicon Epitaxial Planar Transistor

High frequency low noise amplifier application VHF band amplifier application

The transistor is subdivided into three groups R, O and Y, according to its DC current gain

On special request, these transistors can be manufactured in different pin configurations.



Absolute Maximum Ratings ($T_a = 25$ °C)

Parameter	Symbol	Value	Unit	
Collector Base Voltage	V _{CBO}	40	V	
Collector Emitter Voltage	V _{CEO}	30	V	
Emitter Base Voltage	V _{EBO}	4	V	
Collector Current	I _C	20	mA	
Emitter Current	I _E	-20	mA	
Power Dissipation	P _{tot}	100	mW	
Junction Temperature	T _j	150	°C	
Storage Temperature Range	T _{stg}	- 55 to + 150	°C	

Characteristics at T₂ = 25 °C

Characteristics at Ta = 25 C			0	N 4" -	-	N.4 -	11.2
Parameter		Symbol	Min.	Тур.	Max.	Unit	
DC Current Gain							
at $V_{CE} = 6 \text{ V}$, $I_C = 1 \text{ mA}$	Current Gain Group	R	h_{FE}	40	-	80	-
		0	h_FE	70	-	140	-
		Υ	h _{FE}	100	-	200	-
Collector Base Cutoff Current at V _{CB} = 40 V			I _{CBO}	-	-	0.5	μA
Emitter Base Cutoff Current at V _{EB} = 4 V			I _{EBO}	-	-	0.5	μA
Transition Frequency at V _{CE} = 6 V, I _C = 1 mA			f⊤	-	550	-	MHz
Reverse Transfer Capacitance at $V_{CE} = 6 \text{ V}$, $f = 1 \text{ MHz}$			C_{re}	ı	0.7	ı	pF
Collector Base Time Constant at $V_{CE} = 6 \text{ V}$, $I_E = -1 \text{ mA}$, $f = 30 \text{ mB}$	MHz		C _c ∙rbb'	ı	ı	20	ps
Noise Figure at $V_{CC} = 6 \text{ V}$, $f = 100 \text{ MHz}$, $I_E =$	-1 mA		NF	-	2.5	5	dB
Power Gain at $V_{CC} = 6 \text{ V}$, $f = 100 \text{ MHz}$, $I_E = 100 \text{ MHz}$	-1 mA		G_pe	-	18	-	dB









