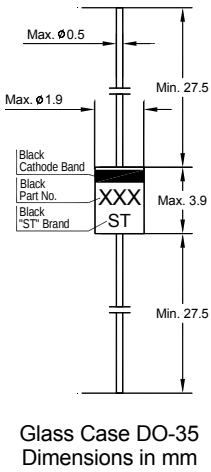


1N746...1N759

SILICON PLANAR ZENER DIODES

Standard Zener voltage tolerance is $\pm 10\%$. Add suffix "A" for $\pm 5\%$ tolerance. Other tolerances, non-standard and higher Zener voltages are upon request.



Absolute Maximum Ratings ($T_a = 25\text{ }^{\circ}\text{C}$)

Parameter	Symbol	Value	Unit
Power Dissipation	P_{tot}	500 ¹⁾	mW
Junction Temperature	T_j	175	$^{\circ}\text{C}$
Storage Temperature Range	T_{Stg}	- 65 to + 175	$^{\circ}\text{C}$
¹⁾ Valid provided that leads are kept at ambient temperature at a distance of 8 mm from case			

Characteristics at $T_a = 25\text{ }^{\circ}\text{C}$

Parameter	Symbol	Max.	Unit
Thermal Resistance Junction to Ambient Air	R_{thA}	0.3 ¹⁾	K/mW
Forward Voltage at $I_F = 200\text{ mA}$	V_F	1.2	V
¹⁾ Valid provided that leads are kept at ambient temperature at a distance of 8 mm from case.			

1N746...1N759

Type	Zener Voltage Range ¹⁾		Maximum Zener Impedance	Maximum Reverse Leakage Current ²⁾ I_R at $V_R = 1$ V		Maximum Regulator Current I_{ZM} (mA)
	V_{Znom} (V)	I_{ZT} (mA)		$T_a = 25$ °C I_R (μA)	$T_a = 150$ °C I_R (μA)	
1N746	3.3	20	28	10	30	110
1N747	3.6	20	24	10	30	100
1N748	3.9	20	23	10	30	95
1N749	4.3	20	22	2	30	85
1N750	4.7	20	19	2	30	75
1N751	5.1	20	17	1	20	70
1N752	5.6	20	11	1	20	65
1N753	6.2	20	7	0.1	20	60
1N754	6.8	20	5	0.1	20	55
1N755	7.5	20	6	0.1	20	50
1N756	8.2	20	8	0.1	20	45
1N757	9.1	20	10	0.1	20	40
1N758	10	20	17	0.1	20	35
1N759	12	20	30	0.1	20	30

¹⁾ Tested with pulses $t_p = 20$ ms
²⁾ Valid provided that leads are kept at ambient temperature at a distance of 8 mm from case.

